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Docket No.: 042390.P6725

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Altug Koker, et al.

Application No.: 09/372,296

Filed: August 11, 1999

For: REDUCING MEMORY ACCESS  
LATENCIES FROM A BUS USING  
PRE-FETCHING AND CACHING

Examiner: Jasmine Song

Art Group: 2187

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"approved"

**REQUEST FOR APPROVAL OF DRAWING CHANGES**

J.S.  
10/15/02

Drawing Processing Branch  
Assistant Commissioner of Patents  
Washington, DC 20231

Sir:

In the Office Action dated April 29, 2002, the Examiner objected to Figure 2. In particular, the Examiner stated that it is not clear if the system includes a cache.

In response, Applicants have amended Figure 2 to change "cache queue" to "data queue" and "cache controller" to "queue controller". Applicants respectfully request acceptance of the amended Figure 2 because no substantive new matter has been added.

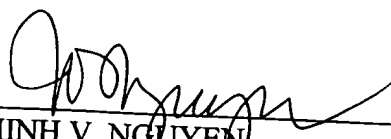
Applicants' undersigned attorney may be reached in the Costa Mesa office by telephone at

(714) 557-3800. All correspondence should continue to be directed to our below listed address.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

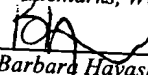
Dated: July 24, 2002

  
THINH V. NGUYEN  
Reg. No. 42,034

12400 Wilshire Boulevard, Seventh Floor  
Los Angeles, California 90025  
(714) 557-3800

**CERTIFICATE OF MAILING**

*I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on July 24, 2002:*

  
Barbara Hayashi



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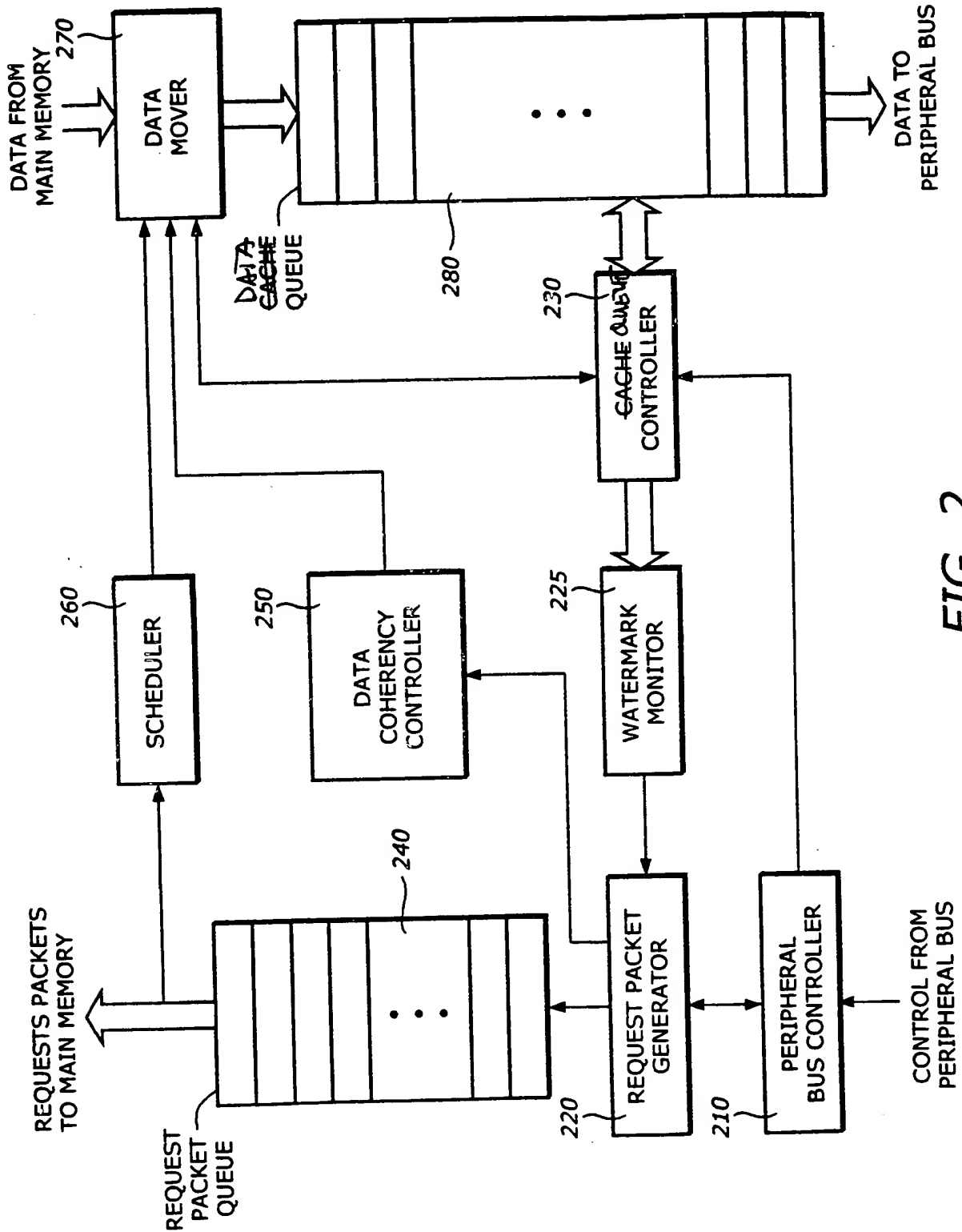


FIG. 2